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1 Promises and reality: Server I/O networks past, present, and future

Renato John Recio

August 2003 **Proceedings of the ACM SIGCOMM workshop on Network-I/O convergence: experience, lessons, implications**

Full text available: pdf(225.62 KB) Additional Information: full citation, abstract, references, index terms

Enterprise and technical customers place a diverse set of requirements on server I/O networks. In the past, no single network type has been able to satisfy all of these requirements. As a result several fabric types evolved and several interconnects emerged to satisfy a subset of the requirements. Recently several technologies have emerged that enable a single interconnect to be used as more than one fabric type. This paper will describe the requirements customers place on server I/O networks; t ...

Keywords: 10 GigE, Cluster, Cluster Networks, Gigabit Ethernet, I/O Expansion Network, IOEN, InfiniBand, LAN, PCI, PCI Express, RDMA, RNIC, SAN, Socket Extensions, TOE, iONIC, iSCSI, iSER

2 Kernel Korner

Joseph Pranevich

December 1998 **Linux Journal**

Full text available: html(22.19 KB) Additional Information: full citation, abstract, index terms

The Wonderful World of Linux 2.2: Mr. Pranevich gives us a look at the changes and improvements coming out in the new kernel

3 Using the SimOS machine simulator to study complex computer systems

Mendel Rosenblum, Edouard Bugnion, Scott Devine, Stephen A. Herrod

January 1997 **ACM Transactions on Modeling and Computer Simulation (TOMACS)**, Volume 7 Issue 1

Full text available: pdf(731.76 KB) Additional Information: full citation, references, citings, index terms, review

Keywords: computer architecture, computer simulation, computer system performance analysis, operating systems

4 Linux on Carrier Grade Web Servers

Ibrahim Haddad, Makan Pourzandi

April 2001 **Linux Journal**

Full text available: html(20.63 KB) Additional Information: full citation, abstract, references, index terms

A great software solution for web traffic problems.

5 Designing computer systems with MEMS-based storage

Steven W. Schlosser, John Linwood Griffin, David F. Nagle, Gregory R. Ganger

November 2000 **Proceedings of the ninth international conference on Architectural support for programming languages and operating systems**, Volume 34 , 28 Issue 5 , 5

Full text available: pdf(439.06 KB) Additional Information: full citation, abstract, references, citings, index terms

For decades the RAM-to-disk memory hierarchy gap has plagued computer architects. An exciting new storage technology based on microelectromechanical systems (MEMS) is poised to fill a large portion of this performance gap, significantly reduce system power consumption, and enable many new applications. This paper explores the system-level implications of integrating MEMS-based storage into the memory hierarchy. Results show that standalone MEMS-based storage reduces I/O stall times by 4-74X over ...

6 Designing computer systems with MEMS-based storage

Steven W. Schlosser, John Linwood Griffin, David F. Nagle, Gregory R. Ganger

November 2000 **ACM SIGPLAN Notices**, Volume 35 Issue 11

Full text available: pdf(439.06 KB) Additional Information: full citation, abstract, references, index terms

For decades the RAM-to-disk memory hierarchy gap has plagued computer architects. An exciting new storage technology based on microelectromechanical systems (MEMS) is poised to fill a large portion of this performance gap, significantly reduce system power consumption, and enable many new applications. This paper explores the system-level implications of integrating MEMS-based storage into the memory hierarchy. Results show that standalone MEMS-based storage reduces I/O stall times by 4--74X ove ...

7 Best of technical support

Linux Journal Staff

October 2002 **Linux Journal**, Volume 2002 Issue 102

Full text available: html(8.10 KB) Additional Information: full citation, index terms

8 The scalability of spatial reuse based serial storage interfaces

Tai-Sheng Chang, Sangyup Shim, David H. C. Du

November 1997 **Proceedings of the fifth workshop on I/O in parallel and distributed systems**

Full text available: pdf(1.05 MB) Additional Information: full citation, references, index terms

9 Trace-driven memory simulation: a survey

Richard A. Uhlig, Trevor N. Mudge

June 1997 **ACM Computing Surveys (CSUR)**, Volume 29 Issue 2

Full text available: pdf(636.11 KB) Additional Information: full citation, abstract, references, citings, index terms, review

As the gap between processor and memory speeds continues to widen, methods for evaluating memory system designs before they are implemented in hardware are becoming increasingly important. One such method, trace-driven memory simulation, has been the subject of intense interest among researchers and has, as a result, enjoyed rapid development and substantial improvements during the past decade. This article surveys and analyzes these developments by establishing criteria for evaluating trac ...

Keywords: TLBs, caches, memory management, memory simulation, trace-driven simulation

10 The DASH prototype: implementation and performance

Daniel Lenoski, James Laudon, Truman Joe, David Nakahira, Luis Stevens, Anoop Gupta, John Hennessy

April 1992 **ACM SIGARCH Computer Architecture News , Proceedings of the 19th annual international symposium on Computer architecture**, Volume 20 Issue 2

Full text available: pdf(1.68 MB) Additional Information: full citation, abstract, references, citings, index terms

The fundamental premise behind the DASH project is that it is feasible to build large-scale shared-memory multiprocessors with hardware cache coherence. While paper studies and software simulators are useful for understanding many high-level design trade-offs, prototypes are essential to ensure that no critical details are overlooked. A prototype provides convincing evidence of the feasibility of the design allows one to accurately estimate both the hardware and the complexity cost of vario ...

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CORPORATE Linux Journal Staff
June 2002 **Linux Journal**, Volume 2002 Issue 98

Full text available: html(7.79 KB) Additional Information: full citation, index terms

12 Application-controlled demand paging for out-of-core visualization
Michael Cox, David Ellsworth
October 1997 **Proceedings of the 8th conference on Visualization '97**

Full text available: pdf(1.46 MB) Additional Information: full citation, references, citings, index terms
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Keywords: computational fluid dynamics, out-of-core visualization, visualization

13 Co-design and synthesis: An efficient system-on-a-chip design methodology for networking applications
Valentina Salapura, Christos J. Georgiou, Indira Nair
September 2004 **Proceedings of the 2004 international conference on Compilers, architecture, and synthesis for embedded systems**

Full text available: pdf(184.23 KB) Additional Information: full citation, abstract, references, index terms

This paper presents a System-on-a-Chip design methodology that uses a microprocessor subsystem as a building block for the development of chips for networking applications. The microprocessor subsystem is a self-contained macro that functions as an accelerator for computation-intensive pieces of the application code, and complements the standard components of the SoC. It consists of processor cores, memory banks, and well-defined interfaces that are interconnected via a high-performance switch. ...

Keywords: network processor, system-on-a-chip

14 Migration: Optimizing the migration of virtual computers
Constantine P. Sapuntzakis, Ramesh Chandra, Ben Pfaff, Jim Chow, Monica S. Lam, Mendel Rosenblum
December 2002 **ACM SIGOPS Operating Systems Review**, Volume 36 Issue SI

Full text available: pdf(1.68 MB) Additional Information: full citation, abstract, references, citings

This paper shows how to quickly move the state of a running computer across a network, including the state in its disks, memory, CPU registers, and I/O devices. We call this state a *capsule*. Capsule state is hardware state, so it includes the entire operating system as well as applications and running processes.We have chosen to move *x*86 computer states because *x*86 computers are common, cheap, run the software we use, and have tools for migration. Unfortunately, *x*86 c ...

15 Editors' choice awards
CORPORATE Linux Journal Staff
December 2001 **Linux Journal**, Volume 2001 Issue 92

Full text available: html(16.96 KB) Additional Information: full citation, abstract

Fame and glory to the winners!

16 Letters
April 2001 **Linux Journal**
Full text available: html(9.28 KB) Additional Information: full citation, index terms

17 Linux in Education: Linux at the University
Kevin K. Gifford
September 2000 **Linux Journal**
Full text available: html(33.25 KB) Additional Information: full citation, abstract, references, index terms

In outer space, on the ground, and in the classroom: an overview of several exciting real-world applications developed under Linuxstudents and researchers at the University of Colorado in Boulder.

18 Missing the memory wall: the case for processor/memory integration
Ashley Saulsbury, Fong Pong, Andreas Nowatzyk
May 1996 **ACM SIGARCH Computer Architecture News , Proceedings of the 23rd annual international symposium on Computer architecture**, Volume 24 Issue 2
Full text available: pdf(1.45 MB) Additional Information: full citation, abstract, references, citings, index terms

Current high performance computer systems use complex, large superscalar CPUs that interface to the main memory through a hierarchy of caches and interconnect systems. These CPU-centric designs invest a lot of power and chip area to bridge the widening gap between CPU and main memory speeds. Yet, many large applications do not operate well on these systems and are limited by the memory subsystem performance.This paper argues for an integrated system approach that uses less-powerful CPUs that are ...

19 Wanted for crimes against the interface: thoughts on an HCI poster
Jef Raskin
December 1996 **interactions**, Volume 3 Issue 6
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20 Construction engineering and project management: Design, development and application of soil transition algorithms for tunneling using special purpose simulation
Janaka Y. Ruwanpura, Simaan M. AbouRizk
December 2001 **Proceedings of the 33nd conference on Winter simulation**
Full text available: pdf(442.78 KB) Additional Information: full citation, abstract, references, index terms

In tunnel construction, the vertical boreholes only show the soil types that are available in the borehole locations. The soil profiles between the boreholes are uncertain and assumed by practitioners for construction purposes. The productivity of the tunnel construction work is therefore affected by adverse soil conditions. The successful implementation of a special purpose tunneling simulation tool identified that the modeling of uncertainties such as soil conditions could provide better resul ...